



(11) **EP 1 178 676 A1**

(12)

EUROPEAN PATENT APPLICATION

(43) Date of publication: 06.02.2002 Bulletin 2002/06

(51) Int CI.7: H04N 5/217

(21) Application number: 01117719.3

(22) Date of filing: 27.07.2001

(84) Designated Contracting States:

AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU

MC NL PT SE TR

Designated Extension States:

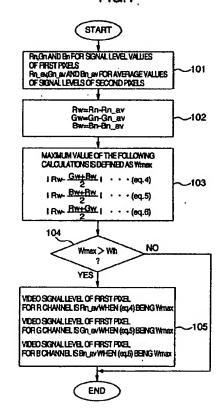
AL LT LV MK RO SI

(30) Priority: 31.07.2000 JP 2000231524

(71) Applicant: Hitachi Kokusai Electric Inc. Tokyo 164-0003 (JP) (72) Inventors:

- Yoshiwara, Kazuhisa, Hitachi Kokusai Electric inc. Tokyo 187-8511 (JP)
- Onodera, Hideo, Hitachi Kokusai Electric Inc. Tokyo 187-8511 (JP)
- (74) Representative: Altenburg, Udo, Dipl.-Phys. Patent- und Rechtsanwälte Bardehle -Pagenberg - Dost - Altenburg - Geissler isenbruck, Galileiplatz 1 81679 München (DE)
- (54) Method and device for detecting defective pixels of a solid-state colour image-pickup apparatus
- A defective pixel detecting method and an image-pickup apparatus having a plurality of solid-state image-plckup devices (3, 4, 5) each receiving respective one of spectral lights obtained by separating light incident to the image-pickup apparatus, generating, for each spectral light, a value relating to an Inspected defective plxel on an associated solid-state image-pickup device located at a corresponding same image-pickup point among the plurality of solid-state image-pickup devices based on a signal level (Rn, Gn, Bn) produced from the inspected pixel and signal levels (Rn-2, Rn-1, Rn+1, Rn+2) produced from a plurality of pixels of the associated solid-state image-pickup device in the vicinity of the inspected pixel, and determining the solid-state image-pickup device (3, 4, 5) that generated a defective pixel based on the values obtained with respect to the plurality of solid-state Image-pickup devices.

FIG.1



Description

10

20

30

40

BACKGROUND OF THE INVENTION

[0001] The present Invention relates to a method of detecting defective pixels produced in a solid-state Image-pickup device such as a charge-coupled device (CCD) used in a television camera and more particularly to a defective pixel detecting method and an image-pickup apparatus having a defective pixel correcting function using the detecting method capable of detecting in real time whether any defective pixel occurs in any image-pickup device or not in the image-pickup apparatus having a plurality of image-pickup devices and in which incident light from an imaging field is separated into R, G, B spectral components respectively and the spectral lights are imaged by the plurality of image-pickup devices.

[0002] A conventional method of detecting defective pixels in solid-state image-pickup devices of a television camera includes, as disclosed in, for example, JP-A-10-98651, optical means for irradiating a photoelectric conversion plane of solid-state image-pickup devices with an optical image from an object to be imaged at every first period, means for reading out electric signal charges of the solid-state image-pickup devices at every second period shorter than the first period, storage means for holding electric signal charges produced from the solid-state image-pickup devices as a reference signal when the photoelectric conversion plane of the solid-state image-pickup devices is not irradiated with the optical image due to a difference between the first and second periods, and means for correcting an image signal constituted by the electric signal charges produced from the solid-state image-pickup devices in accordance with the reference signal when the photoelectric conversion plane of the solid-state image-pickup devices is Irradiated with the optical image.

[0003] According to the above conventional technique, in the same way as a black reference signal is inserted between picture signals by using a rotating shutter disk or electronic shutter, the picture signals can be produced from the solid-state image-pickup devices. As described in the above publication, in a defective pixel correcting circuit, a digitized signal is written in a frame memory as a reference image of a corresponding field during a reference signal term. During other term excepting the reference signal term, a corresponding field image stored in the frame memory is subtracted from the digitized field signal to be produced, so that a defective pixel is corrected while noise having a fixed pattern is detected in real time to thereby eliminate preparation work of defective-pixel data upon fabrication and even if a defective pixel is changed in use, an automatically corrected image can be acquired to improve the image quality.

[0004] With such structure, however, a pause period is produced in an output signal of the defective pixel correcting circuit. Accordingly, when a moving picture is to be viewed in real time, it is necessary to obtain a video signal having a continuous time Interval by passing the output signal through a time-axis expansion circuit. However, in order to adjust a timing by the time-axis expansion circuit, a considerably large-scale circuit configuration must be provided to thereby increase a cost.

[0005] Further, since the video signal corresponding to the aforementioned pause period is not imaged, the contents of the picture are interrupted at the pause period and there is a possibility that the contents of the picture become discontinuous as compared with the required contents of the picture even if the time axis is expanded regardless of the interruption of the contents to make the time interval continuous.

SUMMARY OF THE INVENTION

[0006] It is an object of the present invention to provide a defective pixel detecting method and an image-pickup apparatus for correcting a defective pixel in real time to produce a video signal by using the detecting method capable of detecting a defective pixel in real time by using a video signal imaged during an image signal term without the need of providing a reference signal term separately from the video signal term and without using a large-scale circuit such as the above time-axis expansion circuit as in the prior art.

[0007] In order to achieve the above object, according to an aspect of the present invention, a method of detecting a defective pixel of an image-pickup apparatus having a plurality of solid-state image-pickup devices each receiving a respective one of spectral lights incident to the image-pickup apparatus, comprises the steps of generating a signal level relating to a pixel which is measured whether it is a defective pixel or not (hereinafter called the inspected pixel) on a solid-state image-pickup device on the basis of a signal level produced from an inspected pixel in the solid-state image-pickup device and signal levels produced from a plurality of pixels in the vicinity of such inspected pixel in the solid-state image-pickup device, and determining the defective pixel in the solid-state image-pickup devices on the basis of such signal level.

[0008] The present invention is achieved on the basis of the Inventors' discovery that a video signal level of a normal pixel is the same degree as video signal levels of surrounding pixels thereof in an overwhelming majority and accordingly a value of the video signal level of the normal pixel is the same degree as a average value of the video signal

levels of the surrounding pixels, whereas, for example, when a white noise occurs in a video due to a defective pixel, a value of a video signal level (defect signal level) of the defective pixel (pixel causing white noise) is relatively larger than an average value of video signal levels of the surrounding pixels and a difference value (difference) obtained by subtracting the average value from the value of the defect signal level is also large. Further, the present invention is based on the inventors' further discovery that when a defective pixel is present, only one of a plurality of pixels in the corresponding same image-pickup position among a plurality of solid-state image-pickup devices corresponding to spectral lights for R, G and B in the image-pickup apparatus may be defective, that is, two or more pixels of them may be not defective simultaneously.

[0009] In an embodiment, the value such as the signal level relating to the defective pixel is produced from a difference between the value of the signal level from the inspected pixel and an average value of signal levels from the plurality of pixels in the vicinity of the inspected pixel in the solid-state image-pickup device. The difference is calculated for each spectral light.

[0010] The determining step comprises calculating, for each spectral light, deviations of the difference of the solid-state image-pickup device from average values of differences of at least other solid-state image-pickup devices and comparing the calculated deviations with one another to identify which one of the plurality of solid-state image-pickup devices has generated a defective pixel.

[0011] In an embodiment, when a deviation having a maximum absolute value of the deviations calculated for the plurality of solid-state image-pickup devices is larger than a predetermined threshold value, it is determined that the inspected pixel of the solid-state image-pickup device relating to the deviation having the maximum absolute value is a defective pixel.

[0012] Furthermore, in an embodiment, the plurality of pixels in the vicinity of the inspected pixel include a plurality of pixels adjacent to the inspected pixel on both sides thereof.

[0013] Further, the generating step and the determining step are implemented each time a video signal is produced from the plurality of solid-state image-pickup devices.

[0014] The image-pickup apparatus according to another aspect of the present invention comprises a separator for separating light Incident to the image-pickup apparatus to provide a plurality of spectral lights, a plurality of solid-state Image-pickup devices for receiving the spectral lights to produce video signals respectively, a comparator circuit for comparing a signal level from the inspected pixel on the solid-state image-pickup devices and signal levels from a plurality of pixels in the vicinity of such inspected pixel in order to generate a signal level relating to a defective pixel in the solid-state image-pickup device, a detection circuit for detecting a defective pixel on the plurality of solid-state image-pickup devices on the basis of the signal level obtained from the comparator circuit, a correction circuit responsive to the detection circuit for correcting a signal level from a defective pixel on the solid-state image-pickup device, and a video signal processing circuit for producing a video signal on the basis of corrected signal level from the correction circuit.

[0015] In an embodiment, the correction circuit includes circuits responsive to the detection circuit for replacing the defect signal from the inspected pixel of the solid-state image-pickup device which has generated the defective pixel by an average value of the signal levels from the plurality of pixels in the vicinity of the inspected pixel.

[0016] In a further embodiment, the correction circuit includes circuits responsive to the detection circuit for multiplying the defect signal from the inspected pixel which has generated a defect by a predetermined defective pixel correction coefficient to produce a corrected signal.

[0017] In an embodiment, the image-pickup apparatus includes a control circuit for changing at least one of a threshold for detecting the solid-state image-pickup device which has generated a defective pixel and the defective pixel correction coefficient in accordance with image conditions.

[0018] In an embodiment, the image-pickup apparatus is responsive to an external circuit for controlling whether the defect signal is to be corrected or not in accordance with the level of the defect signal from the inspected pixel which has generated a defect.

BRIEF DESCRIPTION OF THE DRAWINGS

45

55

50 [0019] The foregoing and other objects, features and advantages of the invention will be apparent from the following more particular description of the embodiments of the invention as illustrated in the accompanying drawings wherein:

Fig. 1 is a flow chart showing a defective pixel detection and correction procedure according to an embodiment of the present invention;

Fig. 2 is a block diagram schematically illustrating a television camera according to another embodiment of the present invention;

Flg. 3 is a diagram explaining an example of a correction signal producing method;

Fig. 4 is a diagram showing an example of arrangement of pixels in the vicinity of an inspected pixel used to detect

a defective pixel; and

35

45

Fig. 5 is a block diagram schematically illustrating a defective pixel detecting circuit and a defective pixel correcting circuit used in the television camera of Fig. 2.

DESCRIPTION OF THE EMBODIMENTS

[0020] Embodiments of the present invention are now described with reference to the accompanying drawings. Fig. 2 is a block diagram schematically illustrating a television camera according to an embodiment of the present invention. In Fig. 2, numeral 1 denotes a lens for focusing light incident from an object to be imaged, 2 a prism or separator for separating the incident light passing through the lens 1 into a plurality of spectral lights having wavelengths of, for example, red, green and blue (hereinafter abbreviated as R, G and B, respectively), 3, 4 and 5 solid-state image-pickup devices (CCD) having a plurality of light receiving elements each receiving the spectral light from the prism 2 to convert it into an electric signal in accordance with an amount of spectral light received in each pixel and storing electric charges obtained by the conversion.

[0021] Numerals 6, 7 and 8 denote correlation double sampling (CDS) circuits inputted with video signals produced by successively reading out the electric charges stored in the image-pickup devices 3, 4 and 5 from the light receiving elements and for removing noise components contained in the inputted video signals to sample and hold only signal components, so that the video signals from which the noise components are removed are produced. The CDS circuit is a well-known circuit constituted by a clamp circuit for successively clamping a level of a video signal during a clamping period repeated in a video signal produced from associated solid-state image-pickup devices to a predetermined level and a sample-and-hold circuit for sampling and holding a level during a signal period, of the clamped video signal, although not shown in the drawing.

[0022] Numerals 9, 10 and 11 denote pre-amplifier circuits in which the video signals produced from the CDS circuits 6, 7 and 8 are subjected to video signal processing such as gain correction and gamma correction, and numerals 12, 13 and 14 denote A/D converters for converting the video signals subjected to the video signal processing in the pre-amplifier circuits 9, 10 and 11 into digital signals.

[0023] Numeral 15 denotes a defective pixel detecting circuit for detecting a defective pixel for each pixel on the basis of digital video signals for respective colors produced from the A/D converters 12, 13 and 14 and producing a signal indicative of a position of a detected pixel and a defect signal level of the detected defective pixel. Numeral 16 denotes a defective pixel correcting circuit for correcting the defective pixel of the digital video signals produced from the A/D converter 12, 13 and 14 in accordance with the signal indicative of the detected pixel position and the defect signal level of the defective pixel detected by the defective pixel detecting circuit 15 and producing the corrected video signals to be supplied to a video signal processing circuit 17. In the video signal processing circuit 17, the corrected video signals are subjected to conversion processing for video signal format or the like and supplied to the subsequent stage (not shown).

[0024] Numeral 18 denotes a central processing unit (CPU) for controlling the image-pickup devices 3, 4 and 5 and the aforementioned circuits so as to operate at a predetermined timing and establishing external control between the image-pickup apparatus and an external apparatus (not shown).

[0025] Description is now made to a general situation in which the inventive defective pixel detecting method may be suitably applied in the case where a white noise occurs in a video image due to a defective pixel.

[0026] A defect signal level of a defective pixel causing a white noise appears as a peak component having a signal level higher than video signal levels of surrounding pixels thereof. Accordingly, a value of the video signal (defect signal) level of the defective pixel causing the white noise is relatively larger than an average value of the video signal levels of the surrounding pixels and a difference value (difference) obtained by subtracting the average value from the value of the defect signal level is also large similarly.

[0027] On the other hand, video signal levels of normal pixels having no defect are almost the same as the video signal levels of the surrounding pixels in an overwhelming majority in almost all image-picking up conditions, for example, in conditions where imaging is usually made by an image-pickup apparatus in an almost same viewing condition as the general viewing condition of a human being. In other words, in the case when an image in the range of a view field perceived by a human being is picked up with about the same view angle and from about the same distance of a human being, the image is taken as an image in which chromaticity and brightness between adjacent pixels of the imaged video are relatively smoothly changed, unlike a video image, such as the video produced by computer graphics, which is likely to have a substantial difference in chromaticity and brightness between adjacent pixels. Accordingly, the value of the video signal level of the normal pixel is the same degree as the average value of the video signal levels of the surrounding pixels and the difference value obtained by subtracting the average value from the value of the video signal level of the normal pixel is relatively small.

[0028] Further, for example, when it is assumed that a certain Inspected pixel of an R channel is a white noise causing defective pixel, the possibility that any one or both of pixels of G and B channels at the Image-pickup point of the

incident (imaging) light corresponding to the inspected pixel of R channel are also defective similarly to the inspected pixel of the R channel is very small. That is, it can be safely said that only one pixel of the R, G and B channel pixels corresponding to any image-pickup point in the incident light is defective at the most.

[0029] Accordingly, in an example where an inspected pixel for R channel corresponding to a certain image-pickup point within incident light to be imaged is assumed to have a white noise defect, it is considered that a difference value between a signal level of the inspected pixel of R channel and an average value of signal levels of surrounding pixels thereof is relatively large as described above, and a difference value between a signal level of a pixel of G channel corresponding to the image-pickup point and an average value of signal levels of surrounding pixels thereof and a difference value between a signal level of a pixel of B channel and an average value of signal levels of surrounding pixels thereof are relatively small even in either case of G and B channels.

[0030] Accordingly, respective difference values for R, G and B channels of pixels corresponding to the image-pickup point in the incident light may be processed so as to be compared with one another, for example, so that whether a defective pixel occurs in a pixel for any one of R, G and B channels or not can be detected.

10

35

40

45

50

55

[0031] A defective pixel detecting method according to an embodiment of the present invention is now described. Incident light passing through the lens 1 of the image-pickup apparatus according to the present invention is separated by the prism 2 to obtain spectral lights for R, G and B channels. The spectral lights for the respective channels are received by the solid-state image-pickup devices 3, 4 and 5, respectively. In each solid-state image-pickup device, the spectral light is subjected to photoelectric conversion in accordance with an amount of spectral light received in each pixel to obtain electric charges and the electric charges are stored therein and further outputted successively as a video signal so that the video signals for respective channels are produced. The video signals are supplied through the CDS circuits 6, 7 and 8, the pre-amplifier circuit 9, 10 and 11 and the A/D converters 12, 13 and 14 to the defective pixel detecting circuit 15.

[0032] The defective pixel detecting circuit 15 detects a defect signal portion from a defective pixel of the inputted video signals by using the signal processing procedure as shown in Fig. 1.

[0033] In the detection procedure, video signal level values for pixels (hereinafter referred to as a first pixel in each channel) of the respective channels corresponding to an image-pickup point of interest within incident light to be imaged, for example, an image-pickup point An corresponding to an n-th element of the plurality of light receiving elements arrayed in a matrix on the solid-state image-pickup device are assumed to be Rn, Gn and Bn for the respective channels. Further, a plurality of pixels in the vicinity of the first pixels for the respective channels, that is, pixels (hereinafter referred to as second pixels for each channel) adjacent to or in the vicinity of the first pixels are properly selected and average values of video signal levels of the selected second pixels are calculated. The calculated average values are Rn_av, Gn_av and Bn_av, respectively (step 101 of Fig. 1).

[0034] Fig. 4 shows an example of an arrangement of the second pixels. In Fig. 4, when a certain one of a plurality of pixels arranged side by side on a certain horizontal scanning line of a picture composed of a plurality of horizontal scanning lines is assumed as the first pixel, two pixels adjacent to the first pixel on the right and left side thereof and two pixels adjacent to the two adjacent pixels are defined as the second pixels.

[0035] Then, difference values (differences) for each channel are calculated as Rw, Gw and Bw by subtracting average values of video signal levels of the second pixels obtained above from video signal levels of the first pixels as shown by the following equations 1, 2 and 3, respectively (step 102 of Fig. 1).

Rw=Rn-Rn_av (eq. 1)

Gw=Gn-Gn_av (eq. 2)

Bw=Bn-Bn_av (eq. 3)

[0036] Next, in order to compare the calculated differences Rw, Gw and Bw with one another, deviations for each channel are calculated by subtracting an average value of the differences for channels other than that channel from the difference for that channel and a maximum one of the calculated deviations for each channel is selected as Wmax. The deviations for each channel are expressed by the following equations 4, 5 and 6 in the example shown in Fig. 1 (step 103 of Fig. 1).

for R channel IRw-(Gw+Bw)/2I (eq. 4)

for G channel IGw-(Bw+Rw)/2I (eq. 5)

for B channel IBw-(Rw+Gw)/2i (eq. 6)

[0037] Alternatively, as equations of the deviations for each channel, average values of the differences of three channels for R, G and B may be used as the following equations 7, 8 and 9 to select the maximum value Wmax from the values calculated from these equations.

5

10

15

45

50

for R channel IRw-(Rw+Gw+Bw)/3I (eq. 7)

for G channel IGw-(Rw+Gw+Bw)/3I (eq. 8)

for B channel IBw-(Rw+Gw+Bw)/3i (eq. 9)

20 [0038] After the maximum value Wmax has been obtained as above, it is determined whether the maximum value Wmax is larger than a predetermined threshold value Wth or not (step 104 of Fig. 1).

[0039] The threshold value Wth may be determined such that when the maximum value Wmax is smaller than the threshold value the existence of a defective pixel is hard to be recognized by perception of a human being even if its defective pixel signal is used to display an imaged picture.

[0040] By virtue of determining whether the maximum value Wmax is a value based on the video signal level of a pixel relating to a defective pixel or not, the first pixel of the channel relating to a deviation for the maximum value Wmax can be detected as a defective pixel causing the defective pixel corresponding to the image-pickup point, when the maximum value Wmax is larger than the predetermined threshold value Wth,. On the other hand, when the maximum value Wmax is smaller than the predetermined threshold value Wth, it is determined that no defect occurs in the pixel corresponding to the image-pickup position.

[0041] By implementing the defective pixel detecting procedure of the present Invention described above, when any defective pixel is detected, the defective pixel detecting circuit 15 produces a signal (defective pixel position signal) indicative of a position of the defective pixel based on the detected result to be supplied to the defective pixel correcting circuit 16.

[0042] The defective pixel correcting circuit 16 produces a correction signal on the basis of the inputted defective pixel position signal and the video signals of the surrounding pixels about the defective pixel produced from any of the A/D converters 12, 13 and 14.

[0043] Fig. 3 is a diagram explaining an example of the correction signal producing method. In the example shown in Fig. 3, supposing that Rn is a defect signal level of a defective pixel, a level Rn' of the correction signal thereof is produced by calculating an average value from video signal levels Rn-2, Rn-1, Rn+1 and Rn+2 of four adjacent pixels. In this connection, the correction signal level and the average value of the video signal levels of the second pixels described in the calculation of the aforementioned differences may be calculated by the same equations and in this case calculated by the following equations 10, 11 and 12.

 $Rn_av = (R_{n-2} + R_{n-1} + R_{n+1} + R_{n+2})/4$ (eq. 10)

 $Gn_av = (G_{n-2} + G_{n+1} + G_{n+2})/4$ (eq. 11)

 $Bn_av = (B_{n-2} + B_{n-1} + B_{n+1} + B_{n+2})/4$ (eq. 12)

[0044] Further, it is needless to say that the correction signal levels and the average values are not necessarily required to be the same values and may be calculated by using various calculation methods and combined signals. For example, the defect signal from the first pixel can be multiplied by a predetermined correction coefficient to produce the correction signal. Further, the correction coefficient may be changed.

[0045] Fig. 5 Illustrates an example of a detailed circuit configuration of the defective pixel detecting circuit 15 and the defective pixel defect correcting circuit 16. Referring now to Fig. 5, the defective pixel detecting circuit 15 and the defective pixel correcting circuit 16 are described in more detail.

[0046] In Fig. 5, the digital video signals for R, G and B converted by the A/D converters 12 are supplied to the defective pixel detecting circuit 15 and the defective pixel correcting circuit 16. In the defective pixel detecting circuit 15, the R, G and B digital video signals are supplied to average value producing circuits 150 and delay circuits 152. The average value producing circuits 150 calculate average values of signal levels of the second pixels. The delay circuits 152 delay the inputted R, G and B video signals in accordance with delay amounts caused by average value calculation. The average values and the delayed signals are supplied to comparators 154. In the comparators 154, differences Rw, Gw and Bw are calculated. The calculated differences Rw, Gw and Bw are supplied to a detector 156. In the detector 156, an arithmetic operation unit (1) 157 calculates deviations from the differences Rw, Gw and Bw and selects a maximum value of the calculated deviations as Wmax. Further, an arithmetic operation unit (2) 158 compares the selected maximum value Wmax with a threshold value Wth.

10

30

[0047] In the comparison, when the maximum value Wmax is larger than the threshold value Wth, the first pixel of the video signal for a color of the deviation for the maximum value Wmax is detected as being a pixel in which a defective pixel occurs and the detector 156 produces a video signal selection control signal in accordance with a video signal timing of the first pixel for that color to be supplied to the defective pixel correcting circuit 16. On the other hand, when the maximum value Wmax is smaller than the threshold value Wth, it is regarded that there is no pixel having a defect and the video signal selection control signal is not produced.

20 [0048] In the defective pixel correcting circuit 16, the R, G and B video signals from the A/D converters 12 are inputted through delay circuits 160 to selectors 162 for respective colors. The selectors 162 are further supplied with correction signals produced by correction signal producing units 164 on the basis of the R, G and B video signals supplied from the A/D converters 12. The selectors 162 supplied with the video signals and the correction signals produce the correction signal when the video signal selection control signals produced from the defective pixel detecting circuit 15 are supplied to the selectors. On the other hand, when the control signal is not supplied, the selectors produce the video signals.

[0049] As described above, when Rn is assumed to be the defect signal level of a defective pixel, the correction signal level Rn', in this example Rn_av, produced in accordance with the defective pixel position signal is used as the video signal level of the defective pixel (first pixel) instead of the defect signal level Rn as illustrated in step 105 of Fig. 1 and the defective pixel is corrected by the defect signal level Rn'.

[0050] While the processing procedure for the defective pixel detection and correction for a certain image-pickup point in the incident light to be imaged relating to an inspected pixel has been described, the procedure can be repeated successively while changing a pixel to be inspected successively each time the video signal for each pixel is produced in response to the output operation of the video signal from the solid-state image-pickup device, so that the defective pixel detection and correction can be performed pixel by pixel in real time.

[0051] Further, in the present invention, a signal for setting the defective pixel correction is sent to the CPU circuit 18 by operator's operation using a menu picture not shown of the image-pickup apparatus or by an external control signal produced by using the external control function, so that a signal for controlling the defective pixel detecting circuit 15 and the defective pixel correcting circuit 16 is sent to these circuits 15 and 16 from the CPU circuit 18. With such control, control as to whether correction is made for each defective pixel or not may be made externally and the defect signal level of the defective pixel or the correction coefficient for the defective pixel correction may be changed in accordance with image conditions (presence of storage, storage time, degree of video gain and the like).

[0052] Note that the "presence of storage" mentioned above in connection with the image conditions represents a storage high-sensitive mode imaging or normal imaging. Also, the "storage time" is a time for storing electric charges obtained by irradiating imaging light for picking up one frame image in the storage high-sensitive mode and being subjected to photoelectric conversion. The "degree of video gain" is an actually set gain when an amplification factor of a signal amplifier before D/A conversion is variable.

[0053] As described above, according to the embodiments, the defective pixel detection or correction can be made pixel by pixel in real time by repeating processing successively while a pixel to be inspected is changed each time the video signal for each pixel is produced without the need of providing a reference signal period separately from the video signal period and without using a large-scale circuit such as the defective pixel correcting circuit as in the prior art and the need of previously detecting a defective pixel position and the time of detecting the defective pixel position again and writing it in a memory or the like can be eliminated even if the number of defective pixels is increased in the prior art.

[0054] Further, control as to whether correction is made for each defective pixel in accordance with a defect signal level of a detected defective pixel or not can be made externally and the defect signal level of the defective pixel or the correction coefficient for the defective pixel correction can be changed in accordance with image conditions (presence of storage, storage time, degree of video gain and the like). For example, a white noise detection level (W) can

be changed in accordance with a storage time so that increased and reduced white noise caused by change of the storage time can be corrected properly. In such a manner, correction in accordance with conditions of the imaged picture can be made.

Claims

5

10

15

50

55

 A method of detecting a defective pixel of an Image-pickup apparatus having a plurality of solid-state image-pickup devices (3, 4, 5) each receiving a respective one of spectral lights obtained by separating light incident to said image-pickup apparatus, comprising the steps of:

generating a value (Rw, Gw, Bw) relating to a defect of an inspected pixel on each said solid-state imageplckup device based on a signal level (Rn, Gn, Bn) produced from said inspected pixel and signal levels (Rn-2, Rn-1, Rn+1, Rn+2) produced from a plurality of pixels in the vicinity of said inspected pixel on said solidstate image-pickup device; and

detecting a defective pixel based on said value relating to a defect of said inspected pixel of each said solidstate image-pickup device.

- 2. A method according to Claim 1, wherein the step of generating said value relating to said defect of sald inspected pixel is the step of calculating a difference (Rw, Gw, Bw) between a signal level (Rn, Gn, Bn) from said inspected pixel and an average value (Rn_av, Gn_av, Bn_av) of signal levels from said plurality of pixels in the vicinity of said inspected pixel on said solid-state image-pickup device, said difference being calculated in each solid-state image-pickup device; and
- said detecting step includes the steps of calculating deviations of said difference (Rw, Gw, Bw) of each said solid-state image-pickup device from average values ((Gw+Bw)/2, (Bw+Rw)/2, (Rw+Gw)/2) of differences of at least other solid-state image-pickup devices and comparing said calculated deviations with one another to determine the defective pixel on said solid-state image-pickup device.
- 30 3. A method according to Claim 2, wherein, in said detecting step, when said deviations calculated for said plurality of solid-state image-pickup devices is larger than a predetermined threshold value (Wth), it is detected that said inspected pixel on said solid-state image-pickup device is a defective pixel.
- 4. A method according to Claim 2, wherein said average value of said differences is an average value ((Rw+Gw+Bw)
 35
 35 of differences of said plurality of solid-state image-pickup devices.
 - 5. A method according to Claim 1, wherein said plurality of pixels in the vicinity of said inspected pixel (Rn) include a plurality of pixels adjacent to the pixels in the vicinity of said inspected pixel.
- 40 6. A method according to Claim 1, wherein said generating step and said detecting step are implemented in each time when video signals are produced from said plurality of solid-state image-pickup devices.
 - 7. An image-pickup apparatus comprising:
- a separator (2) for separating light incident to said image-pickup apparatus to provide a plurality of spectral lights;
 - a plurality of solid-state image-pickup devices (3, 4, 5) for receiving said spectral lights to produce video signals respectively;
 - a comparator circuit (150, 152, 154) for comparing a signal level from an inspected pixel and signal levels from a plurality of pixels in the vicinity of said inspected pixel on said sold-state image-pickup devices;
 - a detection circuit (156) for detecting a defective pixel on said plurality of solid-state image-pickup devices based on said signal level obtained from said comparator circuit;
 - a correction circuit (16) responsive to said detection circuit (156) for correcting a signal level from a defective pixel on said solid-state image-pickup device; and
 - a video signal processing circuit (17) for producing a video signal on the basis of the corrected signal level from the correction circuit.
 - 8. An Image-pickup apparatus according to Claim 7, wherein said comparator circuit (150, 152, 154) calculates, in

each sald solid-state image-pickup device, a difference (Rw, Gw, Bw) between a value of the signal level (Rn, Gn, Bn) from said inspected pixel and an average value (Rn_av, Gn_av, Bn_av) of signal levels from said plurality of pixels in the vicinity of said inspected pixel on said solid-state image-pickup device and generates a value relating to said defective pixel, and

5

said detection circuit (156) includes a first arithmetic operation circuit (157) for calculating, in each solid-state image-pichup device, deviations of said difference (Rw, Gw, Bw) of each said solid-state image-pickup device from average values ((Gw+Bw)/2, (Bw+Rw)/2, (Rw+Gw)/2) of differences of at least other solid-state image-pickup devices and comparing said calculated deviations with one another to detect the defective pixel on said solid-state image-pickup device.

10

9. An image-pickup apparatus according to Claim 8, wherein said detection circuit (156) includes a second arithmetic operation circuit (158) for detecting that said inspected pixel is a defective pixel, when said deviations calculated in said plurality of solid-state image-pickup devices is larger than a predetermined threshold value (Wth).

15

10. An image-pickup apparatus according to Claim 8, wherein said average value of said differences is an average value ((Rw+Gw+Bw)/3) of differences of said plurality of solid-state image-pickup devices.

20

11. An image-pickup apparatus according to Claim 7, wherein said plurality of pixels in the vicinity of said inspected pixel include a plurality of pixels adjacent to said pixels in the vicinity of said inspected pixel.

12. An image-pickup apparatus according to Claim 7, wherein said correction circuit (16) includes circuits (160, 162, 164) responsive to said detection circuit (156) for replacing a defect signal from said defective pixel on said solid-state Image-pickup device with an average value (Rn_av, Gn_av, Bn_av) of said signal levels from said plurality of pixels in the vicinity of said defective pixel.

25

13. An image-pickup apparatus according to Claim 9, wherein said correction circuit (16) includes circuits (160, 162, 164) responsive to said detection circuit (156) for multiplying a defect signal from said inspected pixel by a predetermined defect correction coefficient to produce a corrected signal.

30

14. An image-pickup apparatus according to Claim 13, further comprising a control circuit (18) for changing at least one of said predetermined threshold and said defect correction coefficient in accordance with image conditions.

35

15. An image-pickup apparatus according to Claim 14, wherein said image conditions contain at least one of the presence/absence of storage, a storage time and a video gain in said solid-state image-pickup device.

16. An image-pickup apparatus according to Claim 7 further comprising an external circuit (18) for controlling said

defect signal in accordance with a level of said defect signal of said defective plxel.

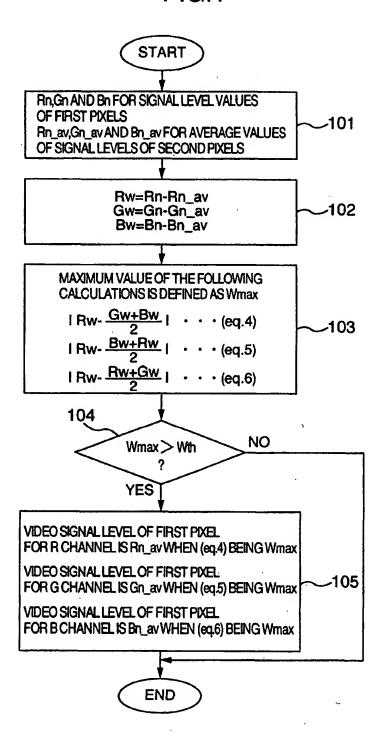
40

45

50

55

FIG.1



EXTERNAL CONTROL SIGNAL VIDEO SIGNAL PROCESSING CIRCUIT DEFECTIVE PIXEL CORRECTING
CIRCUIT SPO DEFECTIVE PIXEL DETECTING CIRCUIT 5 CCD 0 LENS

FIG.3

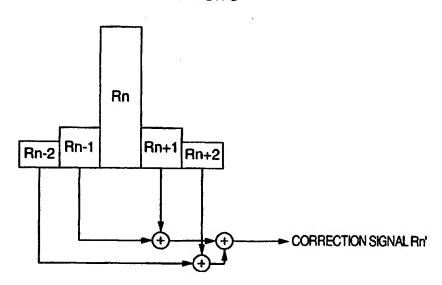
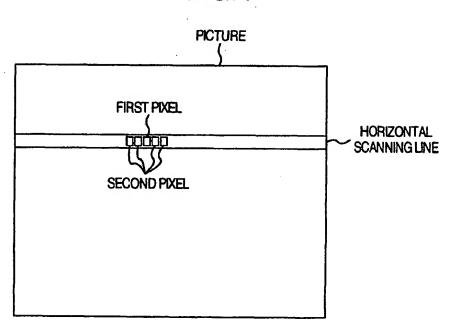
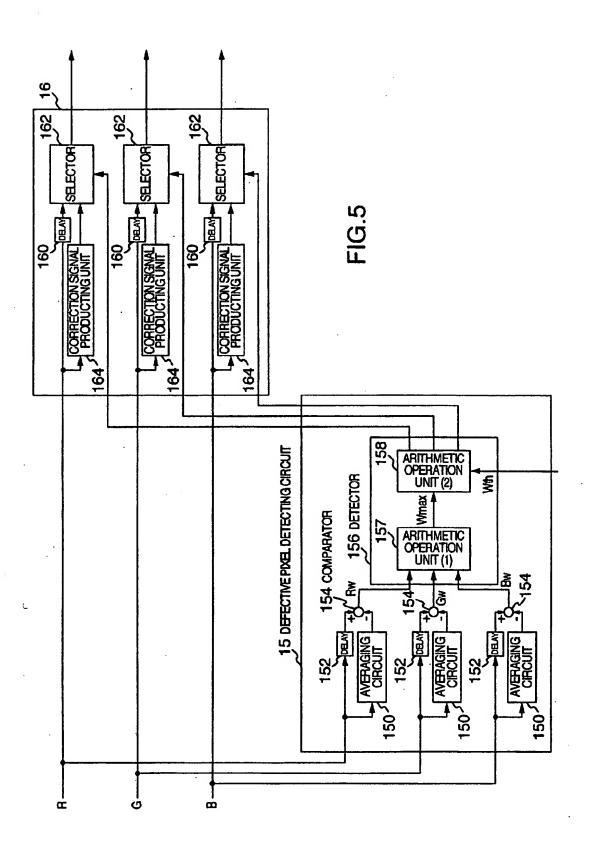


FIG.4







EUROPEAN SEARCH REPORT

Application Number EP 01 11 7719

ategory	Citation of document with indication of relevant passages	n, where appropriate,	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.CL7)
x	EP 0 635 973 A (PHILIPS 25 January 1995 (1995-8	ELECTRONICS NV) 1-25)	1,5-7, 11,12	H94N5/217
A	* column 1, line 37 - c * column 2, line 58 - c	oluman 2, line 36 * oluman 3, line 57 *	8-10	
х .	EP 0 440 563 A (SONY CO 7 August 1991 (1991-08-	RP) 97)	1	
A	* column 18, line 19 - * column 19, line 13 -	line 51 * column 26, line 36	7	
A	PATENT ABSTRACTS OF JAPA vol. 1995, no. 04, 31 May 1995 (1995-05-31 & JP 07 030906 A (MATSU CO LTD), 31 January 1999 * abstract *) SHITA ELECTRIC IND		
A	US 6 081 917 A (FUKUSHII 27 June 2000 (2000-05-2			TECHNICAL FIELDS SEARCHED (Int.CL7)
	·			
	The present search report has been dr	ewn up for all claims Date of compission of the search		Examinor
	THE HAGUE	29 October 2001	Wen	ntzel, J
X : parti Y : parti docu A : tech	ATERORY OF CITED DOCUMENTS cutarly relevant 4 taken alone cutarly relevant 4 combined with another ment of the same category notogical becament written disclosure mediate document	T: theory or princip E: earlier patient di after the filling de D: document cited L: document cited 8: member of the s document	ocument, but publiste in the application for other reasons	shed on, or

ANNEX TO THE EUROPEAN SEARCH REPORT ON EUROPEAN PATENT APPLICATION NO.

EP 01 11 7719

This annex lists the patent family members relating to the patent documents ched in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

29-10-2001

Patent documer cited in search rep	nt xort	Publication date		Patent family member(s)	Publication date
EP 0635973	A	25-01-1995	BE DE DE EP JP US	1007354 A3 69418259 D1 69418259 T2 0635973 A1 7154701 A 6028628 A	23-05-1995 10-06-1999 04-11-1999 25-01-1995 16-06-1995 22-02-2000
EP 0440563	A	07-08-1991	JP JP JP DE DE EP KR	2808781 B2 3227185 A 2808814 B2 3296375 A 69118731 D1 69118731 T2 0440563 A2 188897 B1 5144446 A	08-10-1998 08-10-1991 08-10-1998 27-12-1991 23-05-1996 14-11-1996 07-08-1991 01-06-1999 01-09-1992
JP 07030906	A	31-01-1995	JР	2903956 B2	14-06-1999
US 6081917	A	27-96-2900	JP	9284783 A	31-10-1997
	,				

For more details about this annex : see Official Journal of the European Patent Office, No. 12/82